

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Jean-Yves SIMON, et al.	Confirmation No.:	9476
Serial No.:	10/764,670	Group Art Unit:	2112
Filed:	January 26, 2004	Examiner:	Fritz Alphonse
For:	Method for Streamlining Error Correction Code Computation While Reading or Programming a NAND Flash Memory	Docket No.:	TI-36989 (1962-09800)

**STATEMENT OF SUBSTANCE OF AN INTERVIEW**

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Date: September 23, 2009

Sir:

On September 16, 2009 the Applicants, by and through the undersigned representative, held Examiner-initiated telephonic interview with Examiner Fritz. Claims 1, 6, 12 and 18 were discussed. No prior art was discussed. The Examiner proposed a plurality of amendments which, if authorized, would make the case allowable. In particular, the Examiner suggested rewriting claims 1, 6, 12 and 18 to include dependent claims 2, 7, 13 and 19, respectively. The Examiner also noted that claim 1 is non-statutory because it is not tied to a machine. The Examiner presented US Patent No. 7,171,526 by Cruz as the basis for the amendments. No agreements were reached.

On September 22, 2009 the Applicants, by and through the undersigned representative, held Examiner-initiated telephonic interview with Examiner Fritz. 1, 6, 12 and 18 were discussed. Applicants notified the Examiner that the Applicants are not authorizing the entry of the Examiner's proposed amendments at this time. No prior art was discussed. No agreements were reached.

Respectfully submitted,  
/Utpal D. Shah/

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